

Appln No. 10/621,008

Amdt date March 29, 2004

Reply to Office action of December 31, 2003

**Amendments to the Specification:**

Please amend the paragraph on page 1, beginning on line 6, as follows:

--This application is a continuation of allowed Application No. 10/043,788 filed January 9, 2002, now U.S. Patent No. 6,628,149, which claimed the benefit of the filing date of U.S. Provisional Application Number 60/260,582 entitled "Sub-Micron, high input voltage tolerant I/O circuit" filed January 9, 2001, which is hereby incorporated by reference as though set forth in full, and is related to U.S. Patent Application No. 10/043,763, filed on January 9, 2002.--